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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Toshio YAMADA

Serial No.: 09/779,751

Filed: February 09, 2001



Customer Number: 20277  
 Confirmation Number: 5852  
 Group Art Unit: 2127  
 Examiner: Lilian VO

For: SEMICONDUCTOR INTEGRATED CIRCUIT, COMPUTER SYSTEM, DATA PROCESSOR AND DATA PROCESSING METHOD

Mail Stop Non-Fee Amendment  
 Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

Dear Sir:

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APR 27 2004

Technology Center 2100

Transmitted herewith is an Amendment in the above-identified application.

- No additional fee is required.  
 Applicant is entitled to small entity status under 37 CFR 1.27  
 Also attached:

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	8	20	0	\$18.00 =	\$0.00
Independent Claims	1	6	0	\$86.00 =	\$0.00
Multiple claims newly presented					\$0.00
Fee for extension of time					\$0.00
					\$0.00
Total of Above Calculations					\$0.00

- Please charge my Deposit Account No. 500417 in the amount of \$0.00. An additional copy of this transmittal sheet is submitted herewith.
- The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

MCDERMOTT, WILL &amp; EMERY

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